FIG. 1 2 *-*11 CA CA CA CA 21 PCI BUS 20 18, 28 ⁽31 41 Ol Ol 40 **CPU** 30 **CPU BRD BRD** 25 15 17 **PNB PNB BRIDGE** MEM **MEM BRIDGE** <u> 26 45</u> COMPACT FLASH CF 36 164 44 LAN 33 LAN 32, BIOSBIOS **RSP** 46 BIOSBIOS RSP 34~ 23 24 ·13 DA DA DA DA (FC) (FC) (FC) (FC) 52-1 50 - 1**~ l2** 21-22 -< Q1 <u>-</u>50−2 ⁷52-2 50-m 52-n

FIG. 2

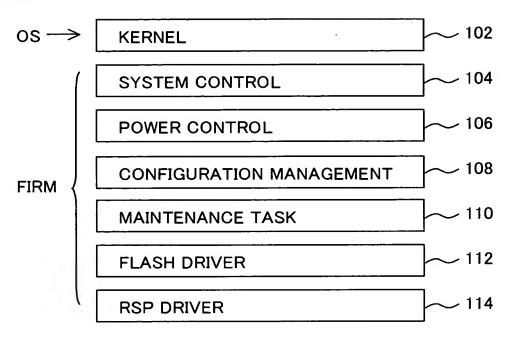


FIG. 3

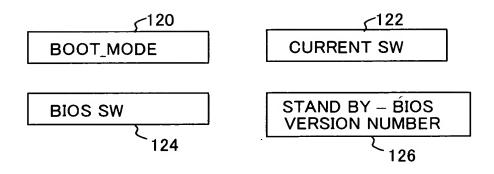
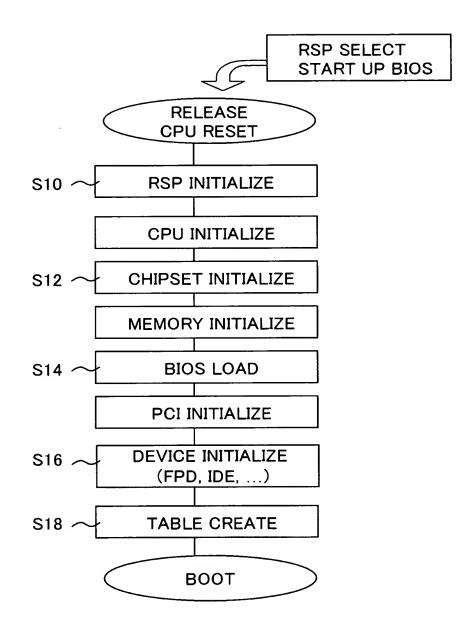
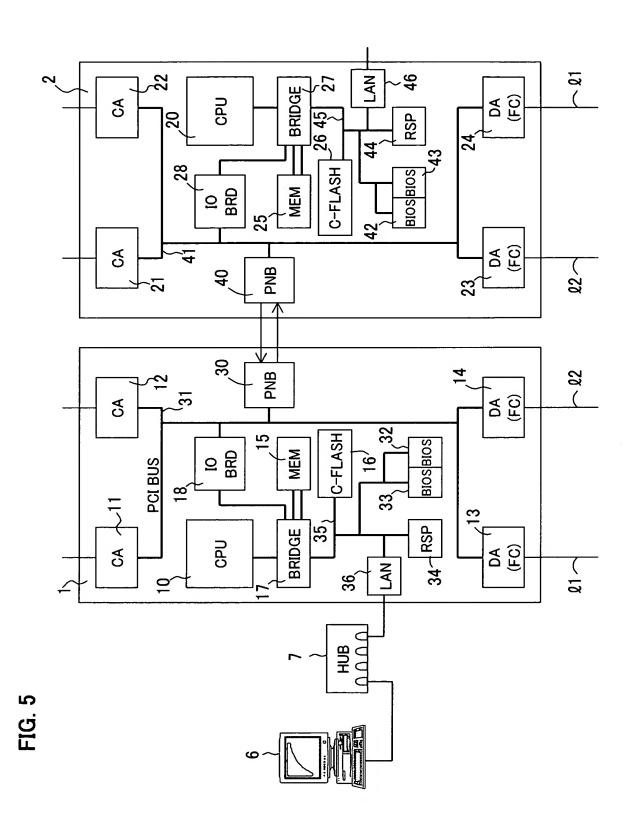
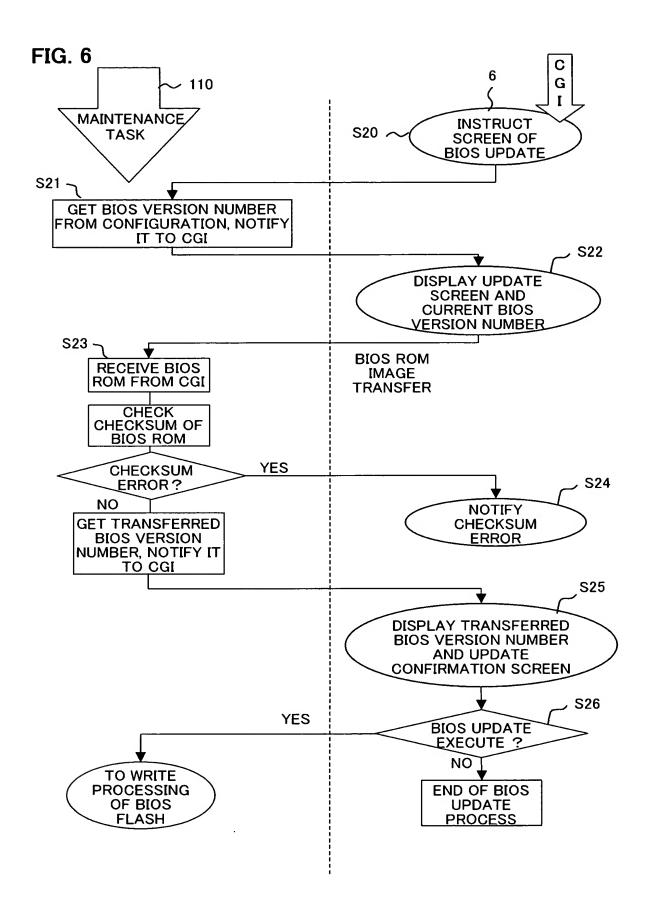


FIG. 4







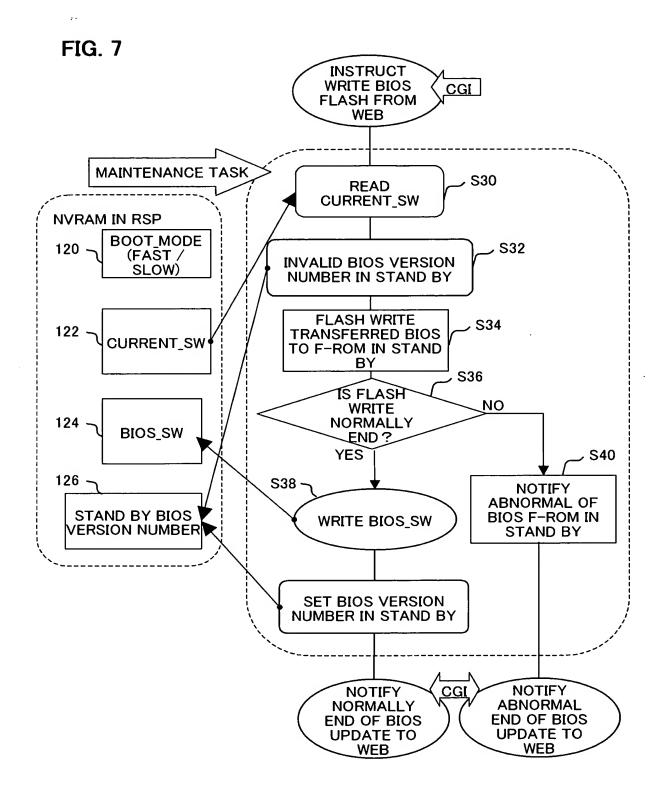
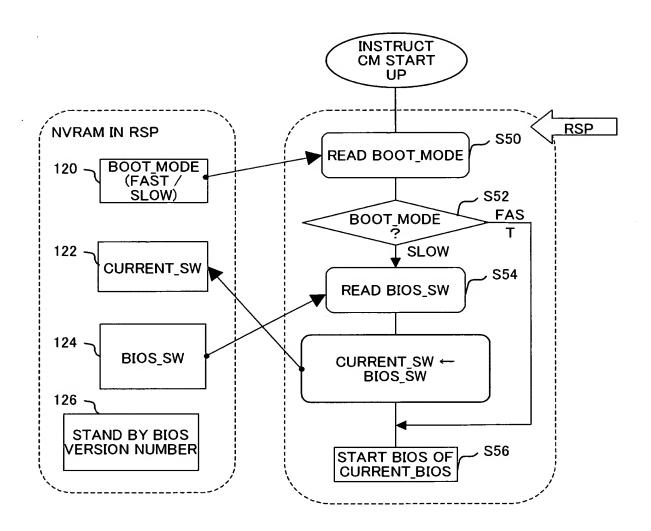


FIG. 8



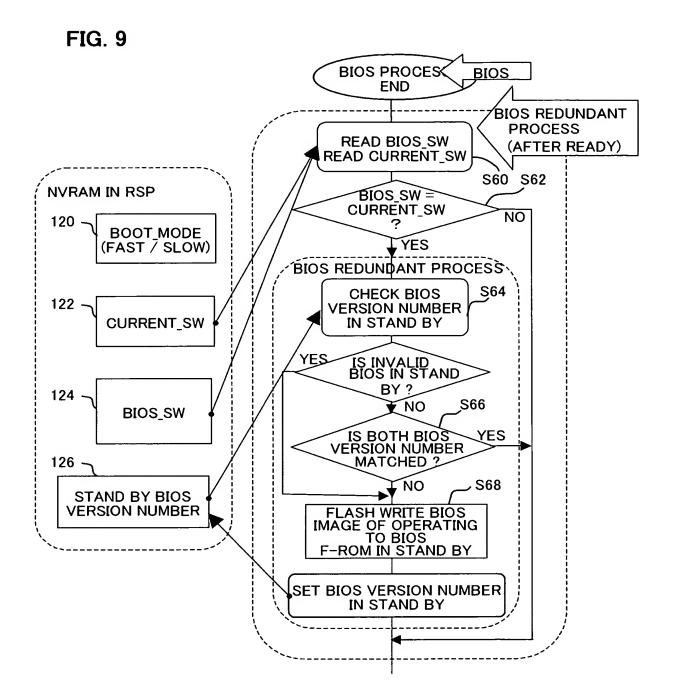


FIG. 10

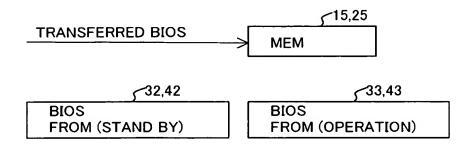


FIG. 11

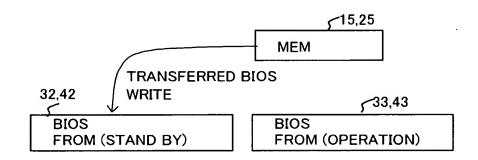
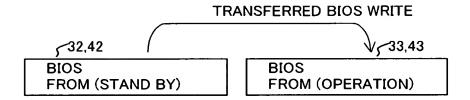


FIG. 12



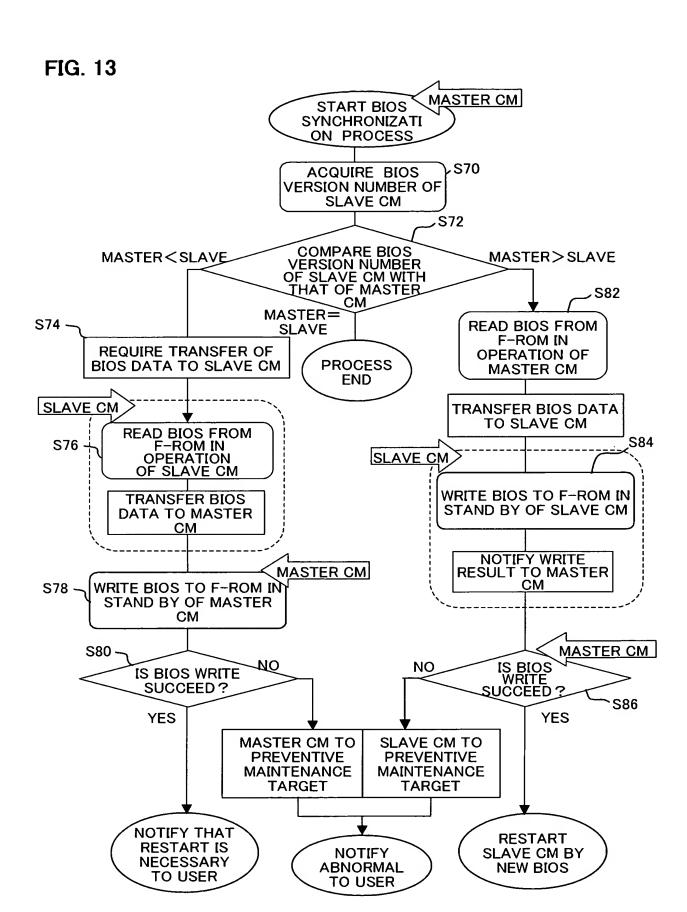


FIG. 14

